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SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

(Sponsored by Sri Vasavi Educational Society)

(Approved by AICTE, New Delhi & Recognized by UGC under section 2(f) & 12(B))

(Permanently affiliated to JNTUK, Kakinada, Accredited by NBA and NAAC with 'A' Grade)

Pedatadepalli, TADEPALLIGUDEM – 534 101.W.G.Dist. (A.P)

Department of Electronics and Communication Engineering

Skill Oriented Program on

“Basic Digital Circuit Design with VHDL and QuestaSim Tool”

**SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)**
Pedatadepalli, TADEPALLIGUDEM - 534101, W.G.Dist, (A.P)



One week Skill Oriented Program on
Basic Digital Circuit Design with
VHDL and QuestaSim Tool

Organized by
SPACE CLUB
Department of Electronics & Communication Engineering
by
Mr. U. Uma Maheswara Rao, Research Scholar, NIT, AP
Under
Scheme for Promotion of Interests, Creativity & Ethics among Students
(SPICES)
Sponsored by
**All India Council for Technical Education (AICTE)**
New Delhi

Date & Timings
27-03-2023 to 01-04-2023
(10:00 AM to 4:30 PM)

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Pedatadepalli, TADEPALLIGUDEM - 534 101.W.G.Dist. (A.P)

Department of Electronics & Communication Engineering**PROGRAM EVALUATION FORM**

Please fill out this form and return to the Department. This form is only used to evaluate the quality/value of the trainers.

Trainer Name: Mr. U. Uma Maheswara Rao, Research Scholar, NIT, AP

Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: S.P.V.L. Gayathri & 22A85A0417

- (1) The Trainers communicated at an appropriate level for the course. [3]
- (2) The Trainers did a good job of relating course concepts to the real world. [2]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [3]
- (4) The Trainers were well prepared. [3]
- (5) The Trainers made the class valuable. [3]
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :

we learned how to design digital circuits using Questa-sim software.

Suggestions (if any):

The humble suggestion from us is to provide a system to each student with respective software so that everyone can learn the software efficiently.

S.P.V.L. Gayathri
Signature of the Student

Vision:

To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and Communication Engineers.

Mission:

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Trainer Name: Mr. U. Uma Maheswara Rao, Research Scholar, NIT, AP

Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: Reddy Mohan Srikanth / 21A81A0414

- (1) The Trainers communicated at an appropriate level for the course. [3]
- (2) The Trainers did a good job of relating course concepts to the real world. [4]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [3]
- (4) The Trainers were well prepared. [4]
- (5) The Trainers made the class valuable. [4]
- (6) Overall, these Trainers were: 4

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :

I learned that how to design a logic gates using VHDL. And I learned that testbench codes clearly for different codes gates.

Suggestions (if any):

My opinion is that the program will continue for 2 more days to understood clearly.

R.M. Srikanth

Signature of the Student

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Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: G. Pralap & 21A81A04F0.

- (1) The Trainers communicated at an appropriate level for the course. [4]
- (2) The Trainers did a good job of relating course concepts to the real world. [4]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [4]
- (4) The Trainers were well prepared. [4]
- (5) The Trainers made the class valuable. [4]
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program : → We learned to able to understand VHDL programme. and implemented programmes like 3x8, decoder, full adder etc.

Suggestions (if any): please - NO. suggestions -

G. Pralap
Signature of the Student

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Trainer Name: Mr. U. Uma Maheswara Rao, Research Scholar, NIT, AP**Skill Oriented Program Name:** Basic Digital Circuit Design with VHDL and QuestaSim Tool**Date & Time:** 27-03-2023 to 01-04-2023**Name of the Organisation:** Sri Vasavi Engineering College**Branch:** ECE**Name of the Student & Reg. No:** CH.VAMSI, 21A81A04EC

- (1) The Trainers communicated at an appropriate level for the course. [Yes] 4
- (2) The Trainers did a good job of relating course concepts to the real world. [Yes] 4
- (3) The Trainers demonstrated a thorough knowledge of the subject. [Yes] 4
- (4) The Trainers were well prepared. [Yes] 4
- (5) The Trainers made the class valuable. [Yes] 4
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :

1. Learn to write own vhdl code & it's simulation
2. Learned codes on flipflops like D. & Test bench codes

Suggestions (if any):

ch.vamsi
Signature of the Student

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- (1) The Trainers communicated at an appropriate level for the course. [*2*]
- (2) The Trainers did a good job of relating course concepts to the real world. [*3*]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [*4*]
- (4) The Trainers were well prepared. [*4*]
- (5) The Trainers made the class valuable. [*4*]
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :*test bench code, architecture Behavioral of counter, IEEE, VHDL, and how to design the VHDL.***Suggestions (if any):***better to give wpt, better to explain in ^{particular} ~~particular~~**P. Jeevanth Kumar*

Signature of the Student

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Trainer Name: Mr. U. Uma Maheswara Rao, Research Scholar, NIT, AP**Skill Oriented Program Name:** Basic Digital Circuit Design with VHDL and QuestaSim Tool**Date & Time:** 27-03-2023 to 01-04-2023**Name of the Organisation:** Sri Vasavi Engineering College**Branch:** ECE**Name of the Student & Reg. No:** 21A81A04H2, P. Sarant.

- | | |
|--|-------|
| (1) The Trainers communicated at an appropriate level for the course. | [4] |
| (2) The Trainers did a good job of relating course concepts to the real world. | [3] |
| (3) The Trainers demonstrated a thorough knowledge of the subject. | [4] |
| (4) The Trainers were well prepared. | [4] |
| (5) The Trainers made the class valuable. | [4] |
| (6) Overall, these Trainers were: | |

1	2	3	4 ✓
Average	Good	Superior	Excellent

Key take away points from this one week Program :

How to design logic gates. flip flops and knowing why we are using some keywords in the program.

Suggestions (if any):

P. Sarant

Signature of the Student

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Trainer Name: Mr. U. Uma Maheswara Rao, Research Scholar, NIT, AP

Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

B.V. Karthikeya, M. Eswar Bharath.

Name of the Student & Reg. No: 21A81A0409, G5

- (1) The Trainers communicated at an appropriate level for the course. [4]
- (2) The Trainers did a good job of relating course concepts to the real world. [3]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [4]
- (4) The Trainers were well prepared. [4]
- (5) The Trainers made the class valuable. [4]
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program : How to design logic gates, FF's etc., and knowing why we are using keywords in coding. easily understanding how to write Test bench codes.

Suggestions (if any):

B.V. Karthikeya

Signature of the Student

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Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: 21-404, 408, 461, 463


- (1) The Trainers communicated at an appropriate level for the course. [3]
- (2) The Trainers did a good job of relating course concepts to the real world. [4]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [4]
- (4) The Trainers were well prepared. [4]
- (5) The Trainers made the class valuable. [3]
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :

- It is a good course for learning
- we felt that, it's a useful course and enjoyed it while learning

Suggestions (if any): - If the programme could be continued for few more days, we will learn more


Signature of the Student

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Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: M. Meghana 21A81A0467

- (1) The Trainers communicated at an appropriate level for the course. [3]
- (2) The Trainers did a good job of relating course concepts to the real world. [3]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [4]
- (4) The Trainers were well prepared. [4]
- (5) The Trainers made the class valuable. [4]
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :

In this program we know about the VHDL using Questasim tool and we perform operations on logic gates flipflops, Mux, decoder, register

Suggestions (if any): No

M. Meghana
Signature of the Student

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Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: M. Sailaja, 21A81A04G3

- (1) The Trainers communicated at an appropriate level for the course. [YES] 4
- (2) The Trainers did a good job of relating course concepts to the real world. [YES] 4
- (3) The Trainers demonstrated a thorough knowledge of the subject. [YES] 3
- (4) The Trainers were well prepared. [YES] 4
- (5) The Trainers made the class valuable. [YES] 3
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :

In this program we learn about VHDL programming and how to write an VHDL code & test bench code. And also I learn how to use questasim software.

Suggestions (if any):

No suggestions.

M. Sailaja
Signature of the Student

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Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: K.G.T.N. Anjali [21A81A04F7] & N. Kavya Sri [21A81A04G8]

- (1) The Trainers communicated at an appropriate level for the course. [4]
- (2) The Trainers did a good job of relating course concepts to the real world. [4]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [4]
- (4) The Trainers were well prepared. [4]
- (5) The Trainers made the class valuable. [4]
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :

we learned how to write VHDL code by using the
questasim tool

Suggestions (if any):

K. Anjali; N. Kavya

Signature of the Student

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Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: p. likhitha [21A81A04H9], S.N.P. Vijaya Lakshmi [4I6], S. Sirisha [4I8]

- (1) The Trainers communicated at an appropriate level for the course. [✓]
- (2) The Trainers did a good job of relating course concepts to the real world. [✓]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [✓]
- (4) The Trainers were well prepared. [✓]
- (5) The Trainers made the class valuable. [43]
- (6) Overall, these Trainers were:

1 Average	2 Good	3 Superior	4 Excellent
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Key take away points from this one week Program :

we have learned few more things about VHDL codings we got some idea about questasim software.

Suggestions (if any):

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Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: G. L. Sowjanya [21A81A04E8] & S. Vijaya [21A81A04J3]

- (1) The Trainers communicated at an appropriate level for the course. [4]
- (2) The Trainers did a good job of relating course concepts to the real world. [4]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [4]
- (4) The Trainers were well prepared. [4]
- (5) The Trainers made the class valuable. [4]
- (6) Overall, these Trainers were:

1	2	3	④
Average	Good	Superior	Excellent

Key take away points from this one week Program :

We learnt how to write VHDL code & how to stimulate it.

Suggestions (if any): —

Sowjanya, *Vijaya*

Signature of the Student

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Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: P. Asritha, R1A81A04H5

- (1) The Trainers communicated at an appropriate level for the course. [4]
- (2) The Trainers did a good job of relating course concepts to the real world. [4]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [4]
- (4) The Trainers were well prepared. [4]
- (5) The Trainers made the class valuable. [4]
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :

Got a brief understanding on implementing and to write a VHDL code.

Suggestions (if any):

P. Asritha
Signature of the Student

Vision:

To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and Communication Engineers.

Mission:

To create a learner centric environment that promotes the intellectual growth of the students.

To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancy practices.

To build the student community with high ethical standards.



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(Sponsored by Sri Vasavi Educational Society)

(Approved by AICTE, New Delhi & Recognized by UGC under section 2(f) & 12(B))

(Permanently affiliated to JNTUK, Kakinada, Accredited by NBA and NAAC with 'A' Grade)

Pedatadepalli, TADEPALLIGUDEM - 534 101.W.G.Dist. (A.P)

Department of Electronics & Communication Engineering

PROGRAM EVALUATION FORM

Please fill out this form and return to the Department. This form is only used to evaluate the quality/value of the trainers.

Trainer Name: Mr. U. Uma Maheswara Rao, Research Scholar, NIT, AP

Skill Oriented Program Name: Basic Digital Circuit Design with VHDL and QuestaSim Tool

Date & Time: 27-03-2023 to 01-04-2023

Name of the Organisation: Sri Vasavi Engineering College

Branch: ECE

Name of the Student & Reg. No: p. Iikhitha [21A81A04H9], S.N.P. Vijaya Lakshmi [4I6], S. Sirisha [4I8]

- (1) The Trainers communicated at an appropriate level for the course. [✓]
- (2) The Trainers did a good job of relating course concepts to the real world. [✓]
- (3) The Trainers demonstrated a thorough knowledge of the subject. [✓]
- (4) The Trainers were well prepared. [✓]
- (5) The Trainers made the class valuable. [43]
- (6) Overall, these Trainers were:

1	2	3	4
Average	Good	Superior	Excellent

Key take away points from this one week Program :

we have learned few more things about VHDL codings we got some idea about questasim software.

Suggestions (if any):

[Signature]

Signature of the Student

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Certification of Participation

This certificate is presented to Garapati Shanthi
studying!!.....year B. Tech in ECE.....branch for active participation one week Skill Oriented Program
on *“Basic Digital Circuit Design with VHDL and QuestaSim Tool”* conducted by Department of
Electronics & Communication Engineering from 27-03-2023 to 01-04-2023 under Scheme for Promotion
of Interests, Creativity & Ethics among Students (SPICES), sponsored by AICTE

U. Uma Maheswari
Trainer,
Research scholar, NIT, AP

E. K. Suman
HOD, ECE



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Certification of Participation

This certificate is presented to *Satti Naga Padma Vijaya Lakshmi*.....
studying *II*.....year B.Tech in *ECE*.....branch for active participation one week Skill Oriented Program
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V. Uma Maheswari
Trainer,
Research scholar, NIT, AP

E. Kusumakumari
HOD, ECE



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Certification of Participation

This certificate is presented to Sanku Chiru V.N.S.S. Vara Prasad.....
studyingyear B.Tech inbranch for active participation one week Skill Oriented Program
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U. Uma Maheswari
Trainer,
Research scholar, NIT, AP

E. K. Sumanth
HOD, ECE



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Certification of Participation

This certificate is presented to Pennumala Ratnakumari.....
studying ...II.....year B.Tech in ECE.....branch for active participation one week Skill Oriented Program
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Trainer,
Research scholar, NIT, AP

E. Ratnakumari
HOD, ECE



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E. K. Srinivas
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Certification of Participation

This certificate is presented to Devam Sri Deepthi Sirisha
studying II year B. Tech in ECE branch for active participation one week Skill Oriented Program
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E. K. Srinivas
HOD, ECE



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Certification of Participation

This certificate is presented to ..Tirumala ViJay Kumar.....
studyingII.....year B. Tech in ..ECE.....branch for active participation one week Skill Oriented Program
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Certification of Participation

This certificate is presented to Pampana Sasant.....
studying ...II.....year B.Tech in ECE.....branch for active participation one week Skill Oriented Program
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E. Kusumakumari
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Certification of Participation

This certificate is presented to Muppana Meghana

studying!!.....year B. Tech inECE.....branch for active participation one week Skill Oriented Program

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Certification of Participation

This certificate is presented to Marri Eswar Bharath

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E. K. Kumar
HOD, ECE

